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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a DIV of 09/388,856 09/01/1999 PAT 6,579,751

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*****IF REQUIRED, FOREIGN FILING LICENSE GRANTED****\*\* 05/24/2001**

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 4	TOTAL CLAIMS 30	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
Verified and Acknowledged				

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**TITLE**

Semiconductor processing methods of forming integrated circuitry

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